

1(a)  $G = \frac{4\pi A_e}{\lambda^2}$

$G$ , gain is the increase in radiated power density in a selected direction relative to that achieved by an isotropic antenna radiating the same total power - expressed as a linear multiple or in dB

$A_e$ , effective aperture determines the power delivered into a matched load by a receiving antenna when an incident power density  $P_i$  falls on the antenna, such that:  
 $P_{rec} = P_i \times A_e = V^2/R \leftarrow$  matched load.

[15%] For a small antenna (dipole) the gain  $\approx 1.5$  (1.76 dB)

(b)(i) power density =  $\frac{4W}{4\pi R^2} \times 1.5$  where range,  $R = 375 \times 10^3$

$\therefore P = \underline{3.40 \text{ pW/m}^2}$

$P = \frac{1}{2} \frac{E^2}{\eta}$  where  $\eta = 120\pi$ , impedance of free space

[15%]  $\therefore E = \sqrt{2P\eta} = \underline{50.6 \text{ } \mu\text{V/m}}$

(ii)  $\lambda = \frac{c}{f} = \frac{3 \times 10^8}{2 \times 10^9} = 0.15 \text{ m}$

$-100 \text{ dBm} \equiv 10^{-13} \text{ W} = A_e P = A_e \cdot 3.4 \times 10^{-12}$

$\therefore A_e = 0.0294 \text{ m}^2$

$G = \frac{4\pi A_e}{\lambda^2} \therefore G = \underline{16.4}$  (or 12.2 dB)

[20%]

(actually not so large, possible with a dish a few feet across)

[10%] (iii)  $10^{-13} = \frac{V^2}{75} \therefore V = \underline{2.74 \text{ } \mu\text{V}_{rms}}$

1(c) Gain = 16.4  $\therefore$  power is concentrated into  $\frac{1}{16.4} \times$  surface area of sphere



$$A_{\text{tot}} = 4\pi R^2 \quad \therefore \quad A_G = \frac{4\pi R^2}{16.4} = \frac{\pi R^2}{4.1}$$

Approximate circle area is sphere patch

$$A_G = \pi (R \sin \theta)^2 \quad \therefore \quad \sin^2 \theta = 0.244$$

$$\therefore \theta = 28^\circ$$

beam  $1/2$ -angle

Hence, total viewing angle across sky for fixed dish  $\approx 56^\circ$ , so call time  $\sim \frac{56}{360} \times 93 = 1.4$  minutes per orbit (if passing directly overhead)

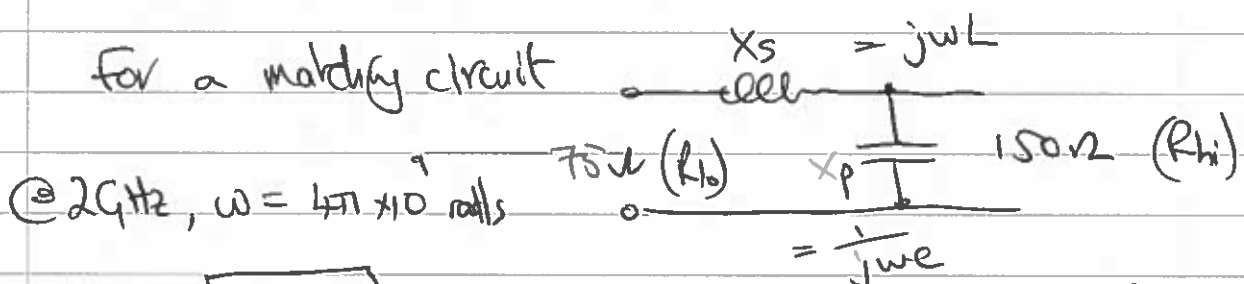
[20%]

(d) with impedance mismatch,  $\rho$ , (voltage refl. coeff) =  $\left| \frac{75-50}{75+50} \right|$

$\therefore$  voltage refl. of 0.33  
(power refl. of 11%)

So, the signal will be reduced  $\therefore$  amplitude and waveforms distorted by reflections, possible standing waves too if the cable impedance is mismatched.

for a matching circuit



@ 2GHz,  $\omega = 4\pi \times 10^9$  rad/s

$$Q = \sqrt{\frac{150}{75}} - 1 = 1 \quad (\text{low } Q, \text{ so broad matching})$$

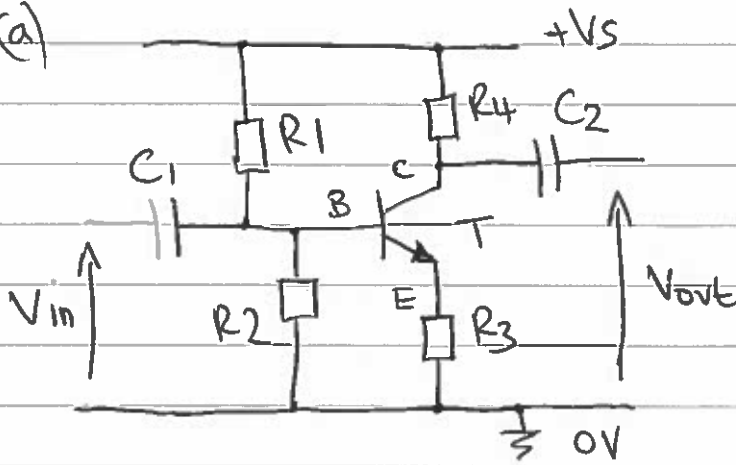
$$Q = \frac{R_{hi}}{X_p} = \frac{X_s}{R_{lo}} \quad \therefore \quad X_p = \frac{1}{4\pi \times 10^9 C} = 150 \quad \therefore C = 0.53 \text{ pF}$$

$$X_s = 4\pi \times 10^9 L = 75 \quad \therefore L = 5.97 \text{ nH}$$

[or 1.06 pF series and 11.9 nH parallel  
i.e. with L and C swapped.]

[20%]

2(a)



$$h_{fe} \approx 250$$

$$V_s = 15V$$

$R_1$  and  $R_2$  set bias voltage on base (dc) for  $V_c \approx \frac{V_s}{2}$

$C_1$  decouples input from dc bias on base

$R_3$  emitter feedback to stabilise bias point and control gain

$R_4$  output resistor: set o/p impedance  $\approx R_4$  and gain  $\approx -\frac{R_4}{R_3}$

[20%]

(b) 20 dB of power gain  $\Rightarrow$  1 transistor should be ok  
 0.1 W into 50  $\Omega$   $\Rightarrow$   $V_{sig} = 2.2$  V<sub>rms</sub> (6.3 V<sub>pp</sub>  $\checkmark$  ok.)  
 $P = V_{sig}^2 / R$

$R_4 = 50 \Omega$   $f = 2$  GHz  $\therefore C_1$  and  $C_2$  choose 10 nF  
 for small impedance re. 50  $\Omega$

$$\tau = 10 / \log_{10} \left( \frac{V_o^2 / 50}{V_i^2 / 75} \right)$$

For 20 dB gain (compared to no amplifier) =  $\times 10$  amplitude, but we add an extra inter-stage coupling with the amplifier, so we design for  $\times 20$  linear voltage gain. ( $\times 16.4$  with some spare margin)

$\therefore R_3 \approx \frac{50}{20} = 2.5 \Omega$   
 initial trial.

now with  $V_c = 7.5$  V d.c. ( $\frac{1}{2}$  supply)

$I_c = 0.15$  A and  $r_e = \frac{0.025}{0.15} = 0.17 \Omega$

Actual gain  $\approx \frac{R_4}{(R_3 + r_e)}$   $\therefore$  set  $R_3 = 2.2 \Omega$  (std value)

(112.5)

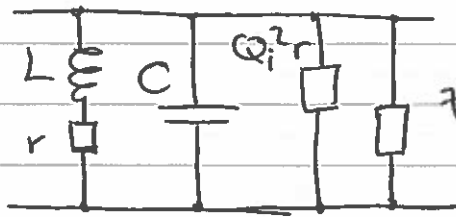
Choose  $R_2 = 15 \times 75 \Omega \approx 110 \Omega$  and  $V_{BE} = 0.65$  V, so with

$V_E = 0.15 \times 2.2 = 0.33$  V, set base at  $\approx 1$  V d.c.

2(b) contd  $\therefore I = \frac{R_2}{R_1 + R_2} \times 15 \therefore R_1 = 1500 \Omega$

[30%] check:  $R_{in} = R_1 \parallel R_2 \parallel h_{ie} \parallel R_3 = 86 \Omega$  ( $\checkmark$  ok for  $75 \Omega$ )

(c)



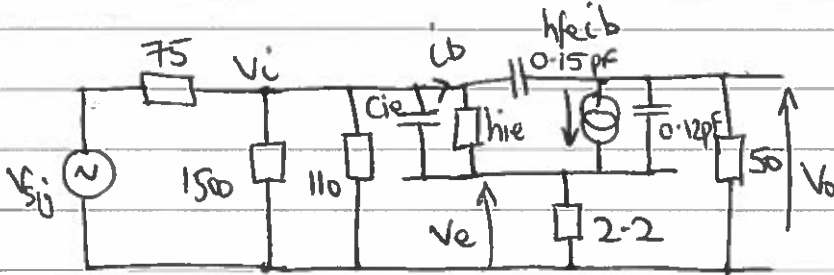
$f_{res} = 2 \times 10^9 = \frac{1}{2\pi\sqrt{LC}}$ ,  $L = 0.47 \mu H$

$\therefore C = 13.5 \text{ pF}$

$Q_i = \frac{\omega L}{r} = 59.1 \therefore Q_i^2 r = 349 \Omega$

[20%]  $\therefore Q_{tot} = \frac{349 \parallel 75 \parallel 86}{\omega L} = \frac{35.9}{5.9} = 6.1 = \frac{2 \times 10^9}{\Delta f} \therefore \Delta f = 328 \text{ MHz}$

(d) Small signal circuit model:



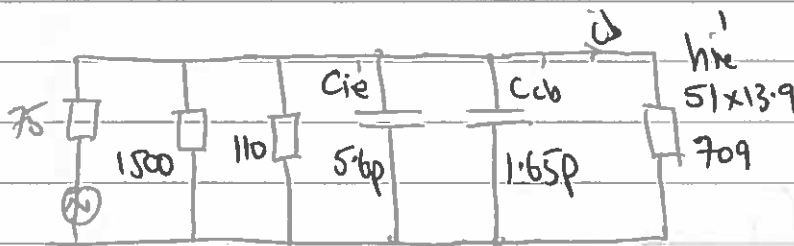
$h_{ie} = h_{fe} r_e = 300 \times 0.17 = 51 \Omega$

$V_e = \frac{R_3}{R_3 + r_e} \cdot V_i = \frac{2.2}{(2.2 + 0.17)} \cdot V_i = 0.928 V_i$

$\therefore \frac{1}{1 - 0.928} = 13.9$ ,  $f_t = 12 \times 10^9 = \frac{1}{2\pi r_e C_{ie}}$

$\therefore C_{ie} = 78 \text{ pF} \div 13.9$  when referred to ground.

Loaded stage gain =  $\times 10$   $\therefore$  input ssm becomes:

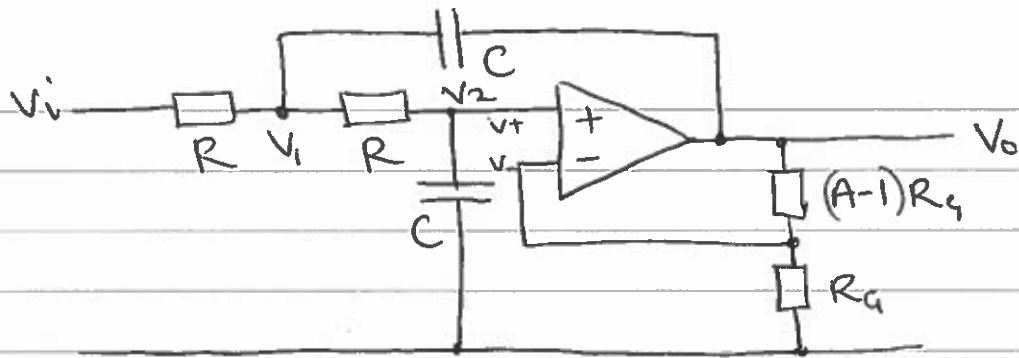


where  $C_{cb}' = 11 \times 0.15 = 1.65 \text{ pF}$  from Miller effect.

Hence  $f_{-3dB}$  is given by:  $\frac{1}{2\pi R' C'}$  where  $R' = 75 \parallel 1500 \parallel 110 \parallel 709 \Omega = 40.8 \Omega$

[30%]  $\therefore f_{-3dB} = 53.8 \text{ MHz}$   $\therefore$  it is not fast enough  $C' = 5.6 + 1.65 \text{ pF}$

3(a)



$$V_o = AV_2 \quad \text{and} \quad V_2 = \frac{V_1 \frac{1}{j\omega C}}{R + \frac{1}{j\omega C}} = \frac{V_1}{1 + j\omega CR} \quad *$$

Sum currents at \$v\_1\$ node:

$$\frac{V_i - V_1}{R} = \frac{V_1 - V_o}{\frac{1}{j\omega C}} + \frac{V_1 - V_2}{R} \Rightarrow V_i = V_1 \frac{(2 + j\omega CR) - V_o j\omega CR - V_2}{R}$$

Subst. for \$V\_1\$ and \$V\_2\$ from \* in above:

$$V_i = \frac{(1 + j\omega CR)}{A} V_o (2 + j\omega CR) - V_o j\omega CR - V_o/A$$

$$\therefore V_i = \frac{V_o}{A} [1 - (\omega CR)^2 + j\omega CR(3 - A)]$$

$$\text{Hence, } \left| \frac{V_o}{V_i} \right| = A \left[ (1 - (\omega CR)^2)^2 + (\omega CR)^2 (3 - A)^2 \right]^{-1/2} \quad \text{for magnitude}$$

$$= A \left[ 1 + (\omega CR)^2 [(3 - A)^2 - 2] + (\omega CR)^4 \right]^{-1/2}$$

with \$\omega = 2\pi f\$ and \$f\_c = \frac{1}{2\pi CR}\$, then we can obtain a Butterworth<sub>2</sub> response when \$(3 - A)^2 - 2 = 0 \therefore \underline{A = 1.586}\$  
(i.e. no \$(f/f\_c)^2\$ terms)

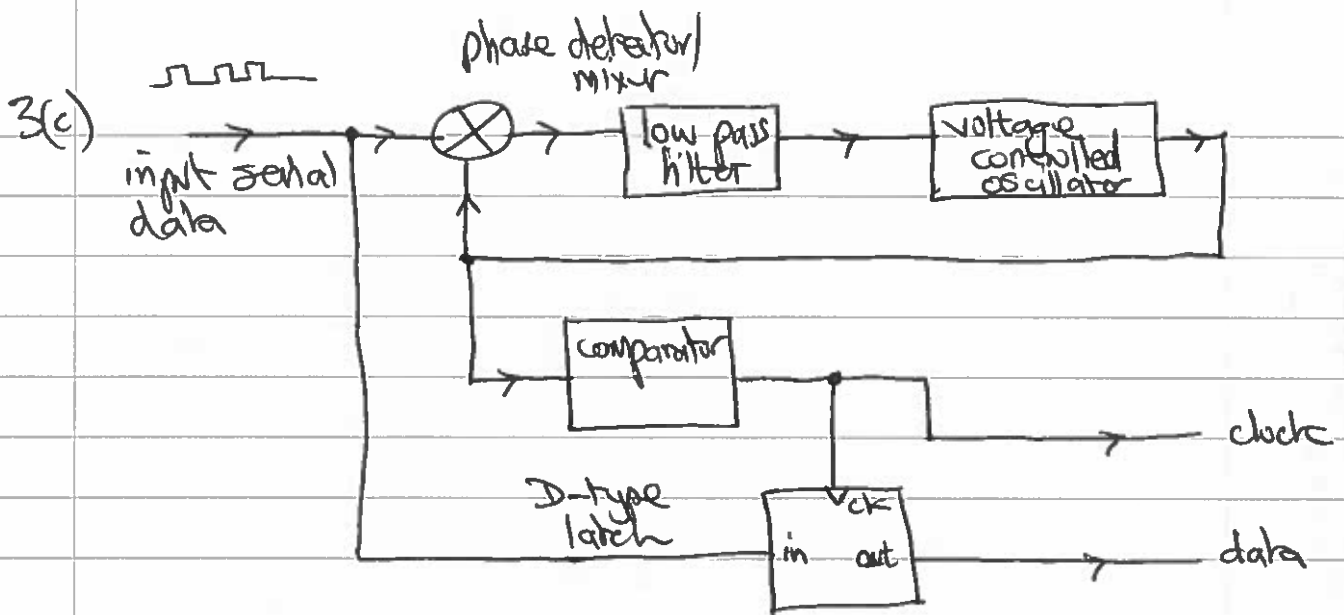
[40%] For a high pass response, the input part \$R\_1\$ and \$C\_1\$ should be swapped over.

(b) Choose a Butterworth response with \$f\_c = 12\$ MHz, so that it passes the fundamental freq. at \$16/2 = 8\$ MHz but is in cut-off for higher harmonics. Butterworth gives \$\sim 10\%\$ overshoot on transition, ok.

select \$R = 100 \Omega\$ and \$R\_c = 1k\Omega\$ then \$(A-1)R\_c = 152 \Omega - \text{①}\$  
and \$1.24k\Omega - \text{②}\$

with 2 cascaded stages of the circuit in (a) above

[20%]  $C = \frac{1}{2\pi f_n R f_c} = \frac{1}{2\pi \times 100 \times 12 \times 10^6} = \underline{133 \text{ pF}}$   
as \$f\_n = 1\$



A comparator is included if the VCO has an analogue output - to synchronize the clock and data streams of bits a D-type latch is included.

The phase detector compares the incoming bitstream with a regular waveform from the VCO. The resolved component from the bitstream fundamental mixes with the VCO output to give a DC component - keeping the frequencies aligned and the 'free-wheeling' inertial effect of the low pass filter allows 'missing' clock bits from the datastream to be tolerated whilst still keeping the loop in lock.

[25%]

(d) 16 Mbit/s  $\Rightarrow$  8 MHz fundamental, but filtered to 12 MHz so we shall work with this frequency

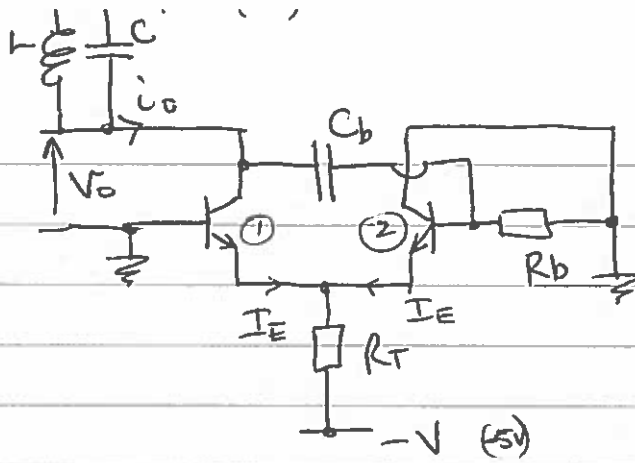
$$\text{skin depth, } \delta = \sqrt{\frac{2}{\omega \mu \sigma}} = \sqrt{\frac{2\rho}{\omega \mu}} = \sqrt{\frac{\rho}{\pi f \mu}}$$

$$\text{For Cu; } \rho = 1.2 \times 10^{-8} \text{ } \Omega\text{m, } \mu = 4\pi \times 10^{-7} \text{ H/m, } \omega = 2\pi f$$

$\therefore$  @ 12 MHz,  $\delta \approx 16 \mu\text{m}$   $\therefore$  choose say  $3 \times \delta$  for minimum losses  $\approx 50 \mu\text{m}$

[15%]

4(a)



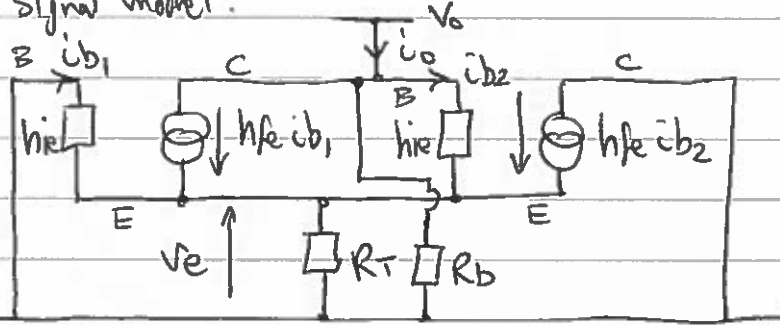
$$f_0 = \frac{1}{2\pi\sqrt{LC}}$$

$$1.5 \times 10^9 = \frac{1}{2\pi\sqrt{10^{-9} \cdot C \cdot 3}}$$

$$\therefore C = 3.75 \text{ pF}$$

$$I_E \approx \frac{(5 - 0.65)}{2R_T}$$

Small signal model:



$$i_{b1} = \frac{-v_e}{h_{ie}} \quad i_{b2} = \frac{v_o - v_e}{h_{ie}} \quad v_e \approx R_T h_{fe}(i_{b1} + i_{b2})$$

$$* \quad i_o = h_{fe} i_{b1} + i_{b2} + v_o/R_b \quad \text{subs. for } i_{b1} \text{ and } i_{b2} \text{ in above:}$$

$$\therefore v_e = -2R_T h_{fe} \frac{v_e}{h_{ie}} + R_T h_{fe} \frac{v_o}{h_{ie}}$$

$$\therefore v_e \left( 1 + \frac{2R_T h_{fe}}{h_{ie}} \right) = R_T h_{fe} \frac{v_o}{h_{ie}} \quad \therefore v_e \approx \frac{v_o}{2}$$

$$\Rightarrow * \quad i_o = -h_{fe} \frac{v_o}{2h_{ie}} + \frac{v_o}{2h_{ie}} + \frac{v_o}{R_b}$$

$$\therefore i_o = v_o \left( \frac{1}{R_b} - \frac{h_{fe}}{2h_{ie}} \right) \quad \therefore Z_o = \frac{v_o}{i_o} = \left( \frac{1}{R_b} + \frac{1}{-2r_e} \right)^{-1}$$

So, output impedance  $\frac{v_o}{i_o} = R_b \parallel -2r_e$

The inductor parasitic resistance (parallel loss) =  $\omega L Q \approx 248 \Omega$

Choose  $R_b = 1k\Omega$   $\therefore$  required  $r_e < 229\Omega$   $\therefore$  say  $100\Omega \approx \frac{0.025}{I_E}$

So  $I_E \geq 0.25 \text{ mA}$   $\therefore R_T = 8700\Omega$  (say  $8.2k\Omega$  std)

[35%]

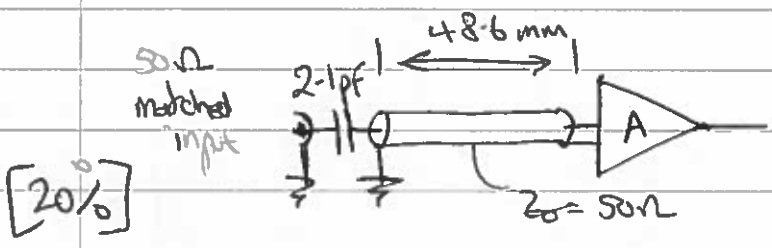
4(b)(i) from Smith chart  $P = 0.35 \angle 40^\circ = 1.50 + j0.80$  normalised  
 [20%]  $\times 50 \Omega$  for actual input impedance =  $75 + j40 \Omega$

(ii) from Smith chart, length of transmission line  $\equiv \frac{330 \times \lambda / 2}{360}$   
 $\lambda = \frac{c}{\sqrt{\epsilon_r} f} = \frac{3 \times 10^8}{\sqrt{2} \cdot 2 \times 10^9} = 0.106 \text{ m}$   
 $\Rightarrow 0.458 \lambda = \underline{48.6 \text{ mm}}$

Impedance 'P' to unity circle 'A' gives  
 $1 + j0.75 \therefore$  requires  $-j0.75$  to cancel Im part / 50

$-j0.75 \times 50 = -j37.5 \Omega = \frac{-j}{2\pi f C}$  with  $f = 2 \times 10^9 \text{ Hz}$

$\therefore C = \underline{2.1 \text{ pF}}$



(ii)  $75 + j40 \Omega$  at  $2 \text{ GHz} \equiv 75 \Omega$  resistor in series with  
 an inductor of  $3.18 \text{ nH}$

at  $1 \text{ GHz}$   $Z_{in} = 75 + j20 \Omega \equiv 1.5 + j0.40$  normalised

[15%]  $\Rightarrow$  Point 'B' on Smith chart  $\Rightarrow$  give  $0.25$  reflection coeff @  $1 \text{ GHz}$

(iv) At  $1 \text{ GHz}$   $\lambda$  is doubled vs  $2 \text{ GHz}$   $\therefore 48.6 \text{ mm} \equiv 0.229 \lambda$   
 which is  $165^\circ$  on the Smith chart.

$\therefore 30^\circ - 165^\circ = -135^\circ$  to point 'C' =  $0.66 - j0.25$

at  $1 \text{ GHz}$ , the series capacitor of  $2.1 \text{ pF}$  gives  $-j75 \Omega = -j1.5$  norm  
 which moves the normalised impedance to  $0.66 - j1.75$ : point D

$\Rightarrow$  read off radius against scale  $\Rightarrow \rho = 0.74$   
 reflection coeff.  
 @  $1 \text{ GHz}$

[10%]

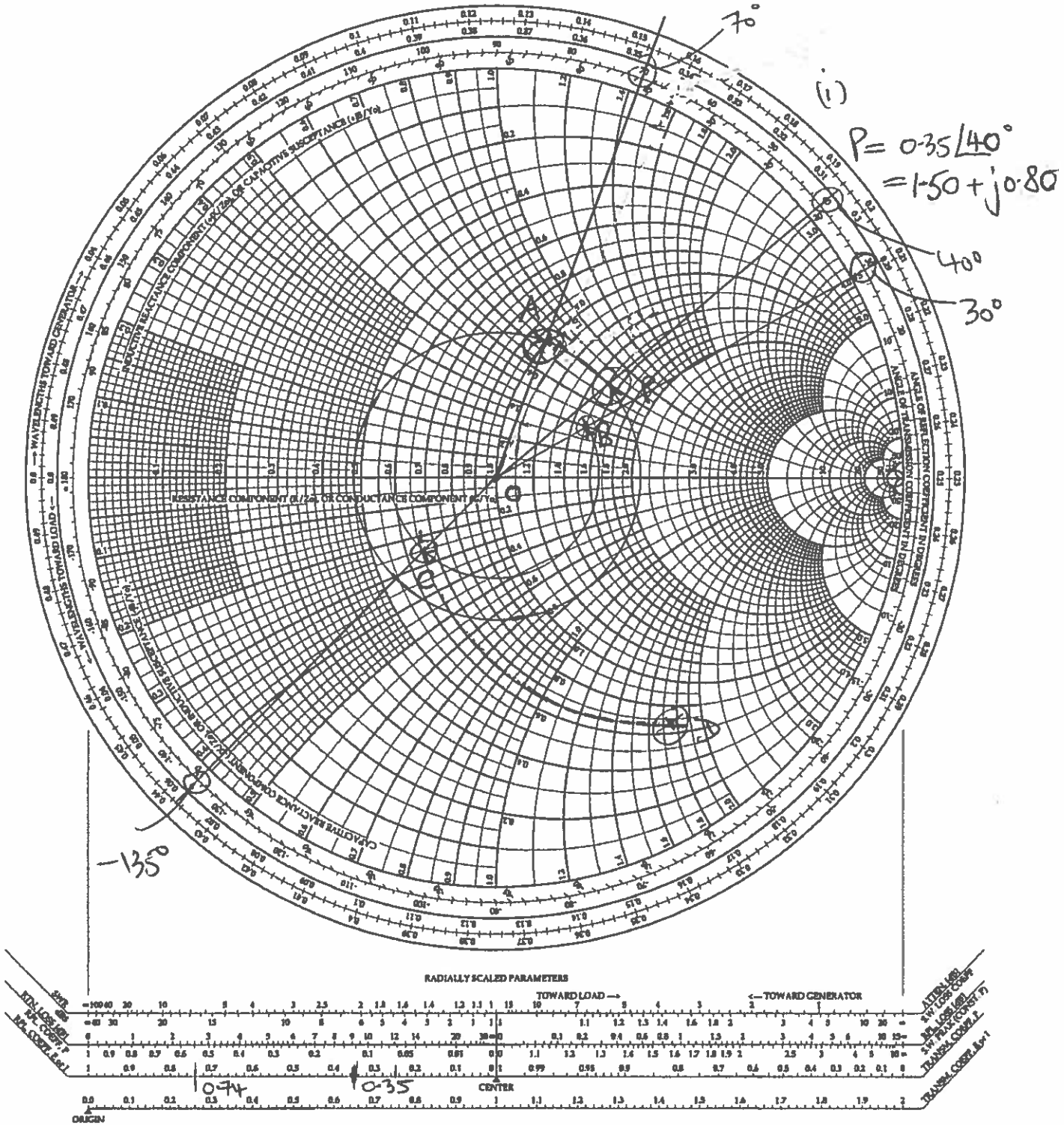


Candidate No.

CP13

4/6

Smith Chart for Question 4 – to be detached and handed in with script.



**Q1 Antennas & impedance matching**

The first part on antenna terms was well answered and similarly so for the signal calculation, although some candidates confused the antenna areas and/or used the effective aperture incorrectly in deriving the beam angle. The final part on matching design was well attempted by most candidates.

**Q2 Transistor RF amplifier**

The basic amplifier circuit was remembered correctly by most candidates and many could select suitable values, although the gain required was often miscalculated; not taking into account the net gain including an extra coupling stage. Most attempts on the resonant circuit parts correctly found the capacitance value, but very few coped with the Q-factor calculation including the stage input and output impedances.

**Q3 VCVS filter, phase locked loop (PLL)**

Another well answered question in general. Most candidates could design the VCVS filter correctly although a number of candidates did not select the correct type (Butterworth). The PLL schematic was recalled correctly in most cases; with some realising that an XOR gate can be used as the phase detector, given the digital nature of the signals. The final part on skin depth was well attempted in general, but few realised that several skin depths are required to minimise conductor resistance.

**Q4 Negative impedance circuit, Impedance matching, Smith chart**

Most candidates correctly drew the negative impedance circuit, but not all managed the bookwork analysis - but some remembered the result. The Smith chart section was generally well attempted for the first part although the following sections presented more of a challenge and there were very few fully correct attempts.

P. A. Robertson (Principal Assessor)