## Part 1(a) paper 32007 crib

## SECTION A

1 (long) (a) Explain briefly how the techniques of mesh current analysis and loop current analysis are used in d.c. and a.c electrical circuits.

Mesh/loop current analysis refers to the application of Kirchhoff's voltage law to solve for unknown currents in a circuit. In mesh analysis the unknown currents flow through the individual circuit elements, and Kirchhoff's current law needs to be applied to enforce current conservation. In loop analysis, the unknown currents flow around each circuit loop, and the currents through the individual elements are appropriate sums of these loop currents. The number of equations and unknowns is equal to the number of independent circuit loops.

Examples:


Mesh analysis:
$\mathrm{KCL} \Rightarrow \mathrm{I}_{3}=\mathrm{I}_{1}+\mathrm{I}_{2}$
Then solve KVLx2


Loop Analysis
Solve KVLx2
(b) Figure 1(a) shows the circuit for an a.c. bridge. The voltage source supplies a sinusoidal waveform of frequency $\omega$. At balance the current through the meter $M$ is zero. Find the condition for balance in terms of $R_{1}, R_{2}, R_{3}, R_{4}$ and the ratio $L / C$.


Fig. 1(a)

Different approaches are possible. Here we give one possible route.

At balance the current though meter M is zero. Thus the voltages at corners $\mathbf{A}$ and $\mathbf{B}$ must be the same. This gives:
$R_{1} R_{4}=\left(R_{2}+j \omega L\right)\left(R_{3} / j \omega C\right) /\left(R_{3}+1 / j \omega C\right)$
$R_{1} R_{4} R_{3 j} \omega C+R_{1} R_{4}=R_{2} R_{3}+j \omega L R_{3}$
$\mathrm{L} / \mathrm{C}=\mathrm{R}_{1} \mathrm{R}_{4}$
$\mathrm{R}_{1} \mathrm{R}_{4}=\mathrm{R}_{2} \mathrm{R}_{3}$
(c) Consider the bridge in Fig. 1(b). Find an expression for the frequency $\omega$ at which the bridge balances, in terms of $R_{1}, R_{2}, R_{3}, R_{4}$ and $C$.


Fig. 1(b)

Again, different approaches are possible. Using the same as 1(b):
$\mathrm{R}_{1}\left(\mathrm{R}_{4}+/ \mathrm{j} \omega \mathrm{C}\right)=\mathrm{R}_{2}\left(\mathrm{R}_{3} / \mathrm{j} \omega \mathrm{C}\right) /\left(\mathrm{R}_{3}+1 / \mathrm{j} \omega \mathrm{C}\right)$
$-R_{1} R_{4} R_{3 j} \omega^{2} C^{2}+\left(R_{4}+R_{3}\right) R_{1} \omega C+j R_{1}=R_{3} R_{2} \omega C$
The real part does not give any indication on $\omega$

Form the imaginary part we get
$\omega^{2}=1 / C^{2} R_{3} R_{4}$

2 (long) (a) Explain what is meant by an ideal operational amplifier, with particular reference to gain, input resistance and output resistance. Explain how the ideal op-amp approximations may be used to simplify the analysis of op-amp circuits.


An ideal op-amp has infinite voltage gain $A_{0}$, infinite input resistance $R_{i}$, and zero output resistance, $\mathrm{R}_{0}$.

The simplifications that the ideal assumptions allow are

1) $A \rightarrow \infty \Rightarrow V_{+}=V$. Virtual earth principle
2) $R_{i} \rightarrow \infty \Rightarrow i_{+}=i_{-}=0$ No current flows into or out of + or -
3) $R_{0} \rightarrow 0 \Rightarrow V_{0}$ is a perfect voltage source, with zero internal resistance. Unaffected by connection of arbitrary loads.
(b) The op-amp in Fig. 2(a) may be assumed as ideal, except for a finite gain $A$. Derive an expression for the voltage gain $V_{2} / V_{I}$ in terms of $R_{1}, R_{2}$ and $A$. Calculate $V_{2} / V_{1}$ if $R_{I}=150 \mathrm{k} \Omega, R_{2}=2 \mathrm{k} \Omega$ and $A=10^{5}$.

Fig. 2(a)

$\mathrm{V}_{1}-\mathrm{V}=\mathrm{V}_{2} / \mathrm{A}$
$\mathrm{V}=\mathrm{R}_{2} /\left(\mathrm{R}_{1}+\mathrm{R}_{2}\right) \mathrm{V}_{2}$
From (2.i) $+(2 . i i)$ we get
$\mathrm{V}_{2} / \mathrm{V}_{1}=\mathrm{A}\left(\mathrm{R}_{1}+\mathrm{R}_{2}\right) /\left(\mathrm{R}_{1}+\mathrm{R}_{2}+\mathrm{AR}_{2}\right)$

Inserting the numerical values in (2b.iii) we get
$\mathrm{V}_{2} / \mathrm{V}_{1}=75.9$
(c) If instead the op-amp has a finite output resistance $R_{0}=150 \Omega$, show that $R_{\text {out }}$, the output resistance of the circuit of Fig. 2(a), is approximately equal to $\underline{R_{0}\left(R_{1}+R_{2}\right) /\left(A R_{2}\right) \text { and calculate } R_{\text {out }} .}$


In order to find $\mathrm{R}_{\text {out }}$, we need to short the input of the op-amp and apply a voltage source V to the output
$\mathrm{V}_{0}=-A V^{\prime}$
$\mathrm{i}=\mathrm{i}_{0}+\mathrm{i}_{2}=\left(\mathrm{V}+\mathrm{AV} \mathrm{V}^{\prime}\right) / \mathrm{R}_{0}+\left(\mathrm{V}-\mathrm{V}^{\prime}\right) / \mathrm{R}_{1}$
$V^{\prime} / R_{2}=\left(V-V^{\prime}\right) / R_{1}$
We get
$\mathrm{R}_{\text {out }}=\mathrm{V} / \mathrm{i}=\mathrm{R}_{0} /\left[1+\mathrm{AR}_{2} /\left(\mathrm{R}_{1}+\mathrm{R}_{2}\right)+\mathrm{R}_{0} /\left(\mathrm{R}_{1}+\mathrm{R}_{2}\right)\right]$
However: $\mathrm{AR}_{2} /\left(\mathrm{R}_{1}+\mathrm{R}_{2}\right) \gg \mathrm{R}_{0} /\left(\mathrm{R}_{1}+\mathrm{R}_{2}\right)+1$, thus
$\mathrm{R}_{\text {out }} \approx \mathrm{R}_{0} / \mathrm{A}\left(1+\mathrm{R}_{2} / \mathrm{R}_{1}\right) \quad$ (2c.i)
Substituting the numerical values in (2c.i) we get:
$\mathrm{R}_{\text {out }} \approx 0.114 \Omega$
(d) A load capacitor C, as shown in Fig. 2(b), is connected to the circuit described in part (c). Calculate $C$ so that $V_{C}$ drops 3 dB from its mid band value at 10 kHz .


Fig 2(b)
The circuit is equivalent to

$\mathrm{V}_{\mathrm{C}}=\mathrm{V}_{1} / \mathrm{j} \omega \mathrm{C} /\left(\mathrm{R}_{\text {out }}+1 / \mathrm{j} \omega \mathrm{C}\right)=\mathrm{V}_{1} /\left(1+\mathrm{j} 2 \pi \mathrm{fCR}_{\text {out }}\right)$
For -3 dB we need $2 \pi \mathrm{fCR}_{\text {out }}=1$

Thus
$\mathrm{C}=1 / 2 \pi \mathrm{fR}_{\text {out }}$
Substituting the numerical values in (2d.i) we get
$\mathrm{C}=139.6 \mu \mathrm{~F}$
3 (short) (a) Define $r_{d}$ and $g_{m}$ in the small-signal a.c. model of a JFET.

$\Delta I_{D}=\left.\frac{\partial I_{D}}{\partial V_{D S}}\right|_{V_{G S}} \cdot \Delta V_{D S}$

$$
\Delta I_{D}=\left.\frac{\partial I_{D}}{\partial V_{G S}}\right|_{V_{D S}} \cdot \Delta V_{G S}
$$

$\Delta I_{D}=\left.\frac{\partial I_{D}}{\partial V_{D S}}\right|_{V_{G S}} \cdot \Delta V_{D S}+\left.\frac{\partial I_{D}}{\partial V_{G S}}\right|_{V_{D S}} \cdot \Delta V_{G S}$
Defining $\left.\frac{1}{r_{d}} \equiv \frac{\partial I_{D}}{\partial V_{D S}}\right|_{V_{G S}}, g_{m}=\left.\frac{\partial I_{D}}{\partial V_{G S}}\right|_{V_{D S}}$
$\mathrm{i}_{\mathrm{d}}=\Delta \mathrm{I}_{\mathrm{D}} \quad \mathrm{V}_{\mathrm{DS}}=\Delta \mathrm{V}_{\mathrm{DS}} \quad \mathrm{V}_{\mathrm{GS}}=\Delta \mathrm{V}_{\mathrm{GS}}$
We get $i_{d}=V_{D S} / r_{d}+g_{m} V_{G S}$
$r_{d}$ is the drain resistance
$\mathrm{g}_{\mathrm{m}}$ is the mutual (or trans) conductance
This describes the following equivalent circuit


The use of the equivalent circuit greatly facilitates the small signal analysis of FET circuits.
(b) Consider the circuit in Fig. 3. Find values for $R_{1}$ and $R_{2}$ required to set the transistor d.c. operating point to $V_{D S}=14 \mathrm{~V}, I_{D}=0.3 \mathrm{~mA}$ and $V_{G S}=-3 \mathrm{~V}$. Assume an ideal transistor and zero gate current.


Fig. 3

DC equivalent circuit


Assuming ideal transistor and zero gate current
$\mathrm{R}_{2} \mathrm{I}_{\mathrm{D}}=-\mathrm{V}_{\mathrm{GS}}$

Substituting the numerical values in (3b.i) we ger
$\mathrm{R}_{2}=10 \mathrm{k} \Omega$
$\mathrm{V}_{\mathrm{R} 1}=30-\mathrm{V}_{\mathrm{DS}}+\mathrm{V}_{\mathrm{GS}}=(30-14-3) \mathrm{V}=13 \mathrm{~V}$
$\mathrm{R}_{1}=\mathrm{V}_{\mathrm{Rl}} / \mathrm{I}_{\mathrm{D}}=43.3 \mathrm{k} \Omega$
(c) Draw the small-signal circuit valid for mid-band frequencies, and find the mid-band small-signal gain, $V_{o} / V_{i}$. For the JFET small-signal parameters take $r_{d}=30 \mathrm{k} \Omega$ and $g_{m}=$ 5 mS .

$\mathrm{V}_{\mathrm{i}}=\mathrm{V}_{\mathrm{GS}}$
$\mathrm{V}_{0}=-\mathrm{g}_{\mathrm{m}} \mathrm{V}_{\mathrm{gS}} \mathrm{R}_{1} \mathrm{r}_{\mathrm{d}} /\left(\mathrm{R}_{1}+\mathrm{r}_{\mathrm{d}}\right)$
Substituting the numerical values in (3c.i) and (3c.ii) we get:
$\mathrm{V}_{0} / \mathrm{V}_{\mathrm{i}}=\mathbf{- 8 8 . 1 3}$

4(short) A small factory consumes 30 kW with a lagging power factor of 0.7 at the factory. When it is connected to a 50 Hz supply the voltage across the factory terminals is 240 V . The supply line has an impedance $Z_{s}=0.02+\mathrm{j} 0.04 \Omega$.
(a) Draw the circuit diagram and calculate the real power lost in the line and the voltage supplied at the input.

$\mathrm{P}=\mathrm{VI} \cos \phi$
$\mathrm{I}=\mathrm{P} / \mathrm{V} \cos \phi=178.6 \mathrm{~A}$
$\mathrm{Q}=\mathrm{P} \tan \phi=30.61 \mathrm{kVAR}$
$\mathrm{P}_{\text {lost }}=\mathrm{I}^{2} 0.02=638 \mathrm{~W}$
$\mathrm{Q}_{\text {lost }}=\mathrm{I}^{2} 0.04=1276 \mathrm{VAR}$
$\mathrm{P}_{\text {in }}=\mathrm{P}+\mathrm{P}_{\text {lost }}=30.638 \mathrm{~kW}$
$\mathrm{Q}_{\mathrm{in}}=\mathrm{Q}+\mathrm{Q}_{\text {lost }}=31.886 \mathrm{kVAR}$
$\mathrm{VA}_{\text {input }}=44.220 \mathrm{kVA}$
$\mathrm{V}_{\text {supply }}=247.6 \mathrm{~V}$
(b) Assuming that the voltage across the factory terminals continues to be 240 V , calculate the required size of the capacitor connected across the factory terminals in order to correct the power factor to unity.
$\mathrm{Q}_{\mathrm{C}}=\mathrm{V}_{\text {load }}^{2} 2 \pi \mathrm{fC}$

Inserting the numerical values
$\mathrm{C}=1.7 \mathrm{mF}$
5 (short) A non-ideal transformer is short-circuit tested and open-circuit tested to measure its characteristics. The open-circuit test gives $V_{\text {primary }}=240 \mathrm{~V}, I_{\text {primary }}=0.2 \mathrm{~A}, \quad P$ $=15 \mathrm{~W}, V_{\text {secondary }}=120 \mathrm{~V}$. The short-circuit test gives $V_{\text {primary }}=40 \mathrm{~V}, I_{\text {primary }}=3 \mathrm{~A}, \quad P=$ 30 W . Determine the values of the equivalent circuit parameters referred to the primary side of the transformer. (See page 20 of the Electrical and Information Data Book for the non-ideal transformer equivalent circuit)

## Open circuit:

Turns Ratio $\mathrm{N}_{1} / \mathrm{N}_{2}=\mathrm{V}_{\text {primary }} / \mathrm{V}_{\text {secondary }}=2$
$S=I_{\text {primary }} V_{\text {primary }}=48 \mathrm{VA}$
$\mathrm{Q}=\left(\mathrm{S}^{2}-\mathrm{P}^{2}\right)^{0.5}=45.6 \mathrm{VAR}$
$\mathrm{R}_{0}=\mathrm{V}_{\text {primary }}^{2} / \mathrm{P}=3.84 \mathrm{k} \Omega$
$\mathrm{X}_{0}=\mathrm{V}_{\text {primary }}^{2} / \mathrm{Q}=1.263 \mathrm{k} \Omega$

## Short Circuit

$\mathrm{S}=\mathrm{V}_{\text {primary }} \mathrm{I}_{\text {primary }}=120 \mathrm{VA}$
$\mathrm{Q}=\left(\mathrm{S}^{2}-\mathrm{P}^{2}\right)^{0.5}=116.2$ VAR

$$
\mathrm{R}_{\mathrm{tl}}=\mathrm{P} / \mathrm{I}_{\text {primary }}^{2}=3.33 \Omega
$$

$$
\mathrm{X}_{\mathrm{tl}}=\mathrm{Q} / \mathrm{I}_{\text {primary }}^{2}=12.91 \Omega .
$$

## 6 (long) There are 3 inputs:

$\mathrm{U}=$ call lift to the top floor,
$\mathrm{D}=$ call lift to the bottom floor,
S = doors shut


There are 4 states so we need 2 flip-flops. Let

$$
\begin{array}{ll}
\text { state } 0=00, & \text { state } 2=10 \\
\text { state } 1=01 & \text { state } 3=11
\end{array}
$$

| $\mathbf{U}$ | $\mathbf{D}$ | $\mathbf{S}$ | $\mathbf{Q}_{\mathbf{A}}$ | $\mathbf{Q}_{\mathbf{B}}$ | $\mathbf{Q}_{\mathbf{A n}}$ | $\mathbf{Q}_{\mathbf{B n}}$ | $\mathbf{J}_{\mathbf{A}}$ | $\mathbf{K}_{\mathbf{A}}$ | $\mathbf{J}_{\mathbf{B}}$ | $\mathbf{K}_{\mathbf{B}}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathbf{X}$ | X | 1 | 0 | 0 | 0 | 0 | 0 | X | 0 | X |  |
| 1 | X | 0 | 0 | 0 | 0 | 0 | 0 | X | 0 | X |  |
| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | X | 0 | X |  |
| 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | X | 1 | X |  |
| 0 | X | 0 | 0 | 1 | 0 | 1 | 0 | X | X | 0 |  |
| X | X | 1 | 0 | 1 | 1 | 0 | 1 | X | X | 1 |  |
| 1 | X | 0 | 0 | 1 | 0 | 0 | 0 | X | X | 1 |  |
| X | X | 1 | 1 | 0 | 1 | 0 | X | 0 | 0 | X |  |
| X | 1 | 0 | 1 | 0 | 1 | 0 | X | 0 | 0 | X |  |
| 0 | 0 | 0 | 1 | 0 | 1 | 0 | X | 0 | 0 | X |  |
| 1 | 0 | 0 | 1 | 0 | 1 | 1 | X | 0 | 1 | X |  |
| X | 0 | 0 | 1 | 1 | 1 | 1 | X | 0 | X | 0 |  |
| X | X | 1 | 1 | 1 | 0 | 0 | X | 1 | X | 1 |  |
| X | 1 | 0 | 1 | 1 | 1 | 0 | X | 0 | X | 1 |  |

State table as above. Karnaugh maps have 5 inputs.
$\underline{\mathrm{J}}_{4}$

$J_{A}=Q_{B} S$
$\underline{K}_{\boldsymbol{A}}$

|  |  |  | , Q |  |  |  |  |  | S, Q |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 00 | 01 | 11 | 10 |  |  | 00 | 01 | 11 | 10 |
|  | 00 | X | 0 | 0 | X | U,D | 00 | X | 0 | 1 | X |
| U,D | 01 | X | 0 | 0 | X |  | 01 | X | 0 | 1 | X |
|  | 11 | X | 0 | 0 | X |  | 11 | X | 0 | 1 | X |
|  | 10 | X | 0 | 0 | X |  | 10 | X | 0 | 1 | X |
| $\mathrm{Q}_{\mathrm{B}}=0$ |  |  |  |  |  |  |  |  | $\mathrm{Q}_{\mathrm{B}}=$ |  |  |

$K_{A}=Q_{B} Q_{A}$
$\underline{\mathbf{J}}_{\mathbf{B}}$

|  |  |  | , Q |  |  |  |  |  | , Q |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 00 | 01 | 11 | 10 |  |  | 00 | 01 | 11 | 10 |
|  | 00 | 0 | 0 | 0 | 0 |  | 00 | X | X | X | X |
| U,D | 01 | 1 | 0 | 0 | 0 |  | 01 | X | X | X | X |
|  | 11 | 0 | 0 | 0 | 0 |  | 11 | X | X | X | X |
|  | 10 | 0 | 1 | 0 | 0 |  | 10 | X | X | X | X |
| $\mathrm{Q}_{\mathrm{B}}=0$ |  |  |  |  |  | $\mathrm{Q}_{\mathrm{B}}=1$ |  |  |  |  |  |

$J_{B}=U \bar{D} \bar{S} Q_{A}+\bar{U} D \bar{S} \bar{Q}_{A}$
$\underline{K}_{\underline{B}}$

|  |  |  | , Q |  |  |  |  |  | , Q |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 00 | 01 | 11 | 10 |  |  | 00 | 01 | 11 | 10 |
|  | 00 | X | X | X | X | U,D | 00 | 0 | 0 | 1 | 1 |
| U,D | 01 | X | X | X | X |  | 01 | 0 | 1 | 1 | 1 |
|  | 11 | X | X | X | X |  | 11 | 1 | 1 | 1 | 1 |
|  | 10 | X | X | X | X |  | 10 | 1 | 0 | 1 | 1 |
| $\mathrm{Q}_{\mathrm{B}}=0$ |  |  |  |  |  | $\mathrm{Q}_{\mathrm{B}}=1$ |  |  |  |  |  |

$K_{B}=S+D Q_{A}+U \bar{Q}_{A}$

Examiner's comments: Sensor S will always be off in states 0 and 2 so one can design the circuit in several ways. Some candidates considered systems with 6 states but realized that 2 are unnecessary when they came to draw up the state table. Some were discouraged by Karnaugh maps with 5 inputs but were able to demonstrate that they understood the principles. Average mark was $64 \%$

## 7 (long)

(a) If $\mathrm{I}_{\mathrm{DS}}=0$, then $\mathrm{V}_{\mathrm{DS}}=-10$, while
if $\mathrm{V}_{\mathrm{DS}}=0$, then $\mathrm{I}_{\mathrm{DS}}=-10 / 500=-20 \mathrm{~mA}$
Plot this load line, and read off at $\mathrm{V}_{\mathrm{GS}}=-10 \mathrm{~V}$. We see that $\underline{V}_{\mathrm{DS}}=-1.3$ volts

(b) $\mathrm{I}_{\mathrm{DS}} \approx-17.5 \mathrm{~mA}$ and $\mathrm{R}=\mathrm{V}_{\text {out }} / \mathrm{I}_{\mathrm{DS}}=5 /\left(-17.5 \times 10^{-3}\right)=\underline{286 \Omega}$
(c) Vary the point at which the load line cuts the vertical axis by $\pm 1 \%$ and we see that the variation in current at the intersection with the line for $V_{G S}=-10 \mathrm{~V}$ is also $1 \%$, so the variation in output will be $1 \%$. So when the gate voltages are as in (b), the error will be 0.05 V

With least significant bit set, output is $5 / 128=0.039 \mathrm{~V}$. Factor equals $0.05 / 0.039=$ 1.28.

Examiner's comment: Most candidates managed part (a) with ease but many found part (b) more difficult and there were few correct answers to part (c). Average mark was $44 \%$.
8. (short).
$(A+C) \cdot(B+D)=A B+B C+A D+C D$
If $\mathrm{A}=\bar{D}$ then

$$
\begin{aligned}
(\mathrm{A}+\mathrm{C}) \cdot(\mathrm{B}+\mathrm{D}) & =\mathrm{AB}+\mathrm{BC}+\mathrm{C} \bar{A} \\
& =\mathrm{AB}+\mathrm{C} \bar{A} \text { (because the term } \mathrm{BC} \text { is redundant) } \\
& =\mathrm{AB}+\mathrm{CD}
\end{aligned}
$$

Alternatively, $\mathrm{B}=\bar{C}$
Examiner's comments: Many candidates showed that the relationship is true if $\mathrm{A}=\overline{\mathrm{D}}$ and $\mathrm{B}=\bar{C}$ : few were able to show that either alone is sufficient. Average mark was $52 \%$.

## 9 (short)



|  | $B \bar{C}$ | $B C$ | $\bar{B} C$ | $\bar{B} \bar{C}$ |
| :---: | :---: | :---: | :---: | :---: |
| $A$ | 0 | 0 | 1 | 0 |
| $\bar{A}$ | 1 | 1 | 1 | 0 |

Figure 9
(a) The truth table implies that $Y=\bar{B} C+\bar{A} B$ but there is a hazard when $A$ is off and $C$ is on and $B$ changes state. As $B$ goes from 0 to 1 , Y will briefly go from 1 to 0 and back, and it will do the same as $B$ goes from 1 to 0 .
(b) Add a third input to the right-hand NAND gate and join to this the output of an extra gate which takes the NAND of $\bar{A} C$.

Examiner's comments: Most candidates got this question correct. Average mark was 78\%.

10 (short) A metal sphere of radius 10 mm is placed with its centre 50 mm above an infinite conducting plane and the sphere is then charged to 50 V . What is the approximate electric field at the conducting plane under the centre of the sphere?



First, find the charge stored on one sphere alone. Let this be $Q$. Then

So:

$$
D=\frac{Q}{4 \pi r^{2}}
$$

$$
E=\frac{Q}{\varepsilon_{0} 4 \pi r^{2}}
$$

So:

So

$$
\begin{aligned}
V & =\int_{R}^{\infty} \frac{Q}{\varepsilon_{0} 4 \pi r^{2}} d r \\
& =\frac{Q}{\varepsilon_{0} 4 \pi R}
\end{aligned}
$$

Where $R$ is the radius of the sphere.

$$
Q=V \varepsilon_{0} 4 \pi R
$$

Now use the method of images and add a sphere with charge $-Q$ whose centre is 100 mm from the centre of the first sphere. Summing the effects of both spheres:

So:

$$
\begin{aligned}
E & =\frac{Q}{\varepsilon_{0} 4 \pi r_{1}^{2}}-\frac{-Q}{\varepsilon_{0} 4 \pi r_{2}^{2}} \quad \begin{array}{l}
\text { Where } r_{1} \text { is } \\
\text { distance from } 1^{\text {st }} \\
\text { sphere, } r_{2} \text { is } \\
\text { distance from } 2^{\text {nd }}
\end{array} \\
& =2 \frac{V \varepsilon_{0} 4 \pi R}{\varepsilon_{0} 4 \pi r^{2}} \quad \begin{array}{l}
\text { Where } R \text { is the } \\
\text { radius of the } \\
\text { spheres and } \\
r=r_{1}=r_{2}
\end{array} \\
& =\frac{2 V R}{r^{2}}=\frac{2 \times 50 \times 10 \times 10^{-3}}{\left(50 \times 10^{-3}\right)^{2}} \\
& =\underline{400 \mathrm{~V} / \mathrm{m}}
\end{aligned}
$$

Examiner's comment: Most candidates recognised that this should be solved by the method of images but then attempted very complicated ways of doing this which usually foundered in a sea of algebra. Average mark was $47 \%$.

11 (short) A straight infinitely long wire is placed in the plane of a square coil and parallel to one side as shown in Fig. 11. Each side of the coil is of length 5 mm . What is the flux through the coil if its centre is 100 mm from the wire which carries a current of 1 A ? If the current in the wire alternates at 50 Hz , approximately how many turns should the coil have to generate an alternating potential of $1 \mu \mathrm{~V}$ across its terminals?


Fig. 11

$$
\begin{array}{rlrl}
H & =I /(2 \pi r) & & =1 /\left(2 \times \pi \times 10^{-1}\right)=1.59 \mathrm{H} / \mathrm{m} \\
B & =\mu_{0} H & & =4 \pi \times 10^{-7} \times 1.59=2 \times 10^{-6} \mathrm{~T} \\
\phi_{0} & =B \times\left(5 \times 10^{-3}\right)^{2} & & =2 \times 10^{-6} \times\left(5 \times 10^{-3}\right)^{2}=50 \times 10^{-12} \mathrm{~Wb} \\
V & =n \frac{d}{d t}\left(\phi_{0} \sin (2 \pi f t)\right) & & \begin{array}{l}
\text { Where } n \text { is the number of turns in } \\
\text { the coil and } f, \text { the frequency },
\end{array} \\
& =n \phi_{0} 2 \pi f \cos (2 \pi f t) & & \text { equals } 50 \mathrm{~Hz} . \\
V_{p} & =n \phi_{0} 2 \pi f & & \text { Where } V_{p} \text { equals the peak voltage } \\
n & =\frac{V_{P}}{\phi_{0} 2 \pi f}=\frac{10^{-6}}{50 \times 10^{-12} \times 2 \pi \times 50} & & \\
& =\underline{64 \text { turns }} &
\end{array}
$$

Examiner's comment: Most candidates were able to derive a formula for the magnetic field as a function of distance from the wire. The setter had (correctly) assumed that, given the coil was small compared to the distance from the wire, the magnetic field could be approximated as constant over the coil. Most students recognised that the field did vary and attempted to integrate the flux over the coil - most failing to set up the integral correctly. Both ways were allowed full marks (indeed only giving $0.1 \%$ difference in the answer). Most candidate's who got that far recognised that Faraday's law should be used for calculating the number of turns in the coil and could quote the law. However, the majority were unable to set up the expression for the alternating flux correctly and of those who could, few were able to differentiate correctly. Average mark was 54\%.

12 (short) A 2 mm thick iron plate has a relative permeability of 1000 . It is cut into a bar within an annulus, both of which are 10 mm wide as shown in Fig. 12. The annulus has an average diameter of 50 mm . If 0.5 A is passed through a coil of 300 turns round the bar and 2 mm gaps are cut between the top and bottom halves of the annulus either side of the coil, what is the magnetic field in the gaps? State any assumptions made.


Fig. 12

Assume that H is uniform across the 10 mm width of the bar and similarly for the annulus. Integrate H round the dotted loop shown:

$$
\begin{aligned}
\Sigma H L & =N I \\
\left(2 B_{A} / \mu_{0} \mu_{r}\right) \times 50 \times 10^{-3}+\left(B_{A} / \mu_{0} \mu_{r}\right)(\pi \times 25-2) \times 10^{-3}+\left(B_{A} / \mu_{0}\right) \times 2 \times 10^{-3} & =300 \times 0.5 \\
B_{A} \times 2.18 \times 10^{-3} & =150 \mu_{0} \\
B_{A} & =\underline{86 \mathrm{mT}}
\end{aligned}
$$

Quite an easy question if the candidate knew how to do it. Those who did scored highly but there was a significant proportion who only had a basic idea and could not set up the equations correctly. A common error was not to recognise that the flux in the central bar was shared by the two arms. Average mark was $57 \%$.

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